

L Number	Hits	Search Text	DB	Time stamp
1	221	(327/218).CCLS.	USPAT; US-PGPUB	2004/08/16 09:48
2	317	(327/202).CCLS.	USPAT; US-PGPUB	2004/08/16 09:48
4	294	((327/202).CCLS.) not ((327/218).CCLS.)	USPAT; US-PGPUB	2004/08/16 10:26
5	2	("5777489" "6433586").PN.	USPAT	2004/08/16 10:03
6	8	("4063225" "4521702" "4716312" "5162667" "5302866" "5909127" "6069495" "6194914").PN.	USPAT	2004/08/16 10:09
7	3	6433586.URPN.	USPAT	2004/08/16 10:10
8	212	(327/203).CCLS.	USPAT; US-PGPUB	2004/08/16 10:27
9	116	((327/203).CCLS.) not (((327/218).CCLS.) ((327/202).CCLS.))	USPAT; US-PGPUB	2004/08/16 10:34
10	2032	latch\$3 with (power adj consumption sleep active normal inactive) adj (state mode)	USPAT; US-PGPUB	2004/08/16 14:17
11	152	(latch\$3 with (power adj consumption sleep active normal inactive) adj (state mode)) with transistor\$1	USPAT; US-PGPUB	2004/08/16 10:35
12	140	((latch\$3 with (power adj consumption sleep active normal inactive) adj (state mode)) with transistor\$1) not (((327/218).CCLS.) ((327/202).CCLS.) ((327/203).CCLS.))	USPAT; US-PGPUB	2004/08/16 10:36
13	921	latch\$3 with restore	USPAT; US-PGPUB	2004/08/16 12:24
14	342	(latch\$3 with restore) and transistor	USPAT; US-PGPUB	2004/08/16 13:47
15	11	("4503494" "4980859" "5257223" "5532958" "5682345" "5844422" "5889697" "5986962" "6034886" "6091626" "6118689").PN.	USPAT	2004/08/16 12:53
16	5550	latch same (power adj consumption sleep active normal inactive)	EPO; JPO; DERWENT; IBM_TDB	2004/08/16 14:12
17	548	(latch same (power adj consumption sleep active normal inactive)) and transistor	EPO; JPO; DERWENT; IBM_TDB	2004/08/16 13:48
18	826	latch same restor\$3	EPO; JPO; DERWENT; IBM_TDB	2004/08/16 14:12
19	802	(latch same restor\$3) not ((latch same (power adj consumption sleep active normal inactive)) and transistor)	EPO; JPO; DERWENT; IBM_TDB	2004/08/16 14:12
20	66	((latch same restor\$3) not ((latch same (power adj consumption sleep active normal inactive)) and transistor)) and transistor	EPO; JPO; DERWENT; IBM_TDB	2004/08/16 14:14
21	611	data adj latch.ti.	EPO; JPO; DERWENT; IBM_TDB	2004/08/16 14:14
22	48	(data adj latch.ti.) and transistor	EPO; JPO; DERWENT; IBM_TDB	2004/08/16 14:14
23	0	latch\$3 with (standby stand adj by) adj (state mode)	USPAT; US-PGPUB	2004/08/16 14:17
24	369	latch\$3 with (standby stand adj by) adj (state mode)	USPAT; US-PGPUB	2004/08/16 14:54
25	7681	FLIP adj flop with (sleep power adj down active normal standby stand adj by)	USPAT; US-PGPUB	2004/08/16 14:55
26	729	(FLIP adj flop with (sleep power adj down active normal standby stand adj by)) same transistor	USPAT; US-PGPUB	2004/08/16 14:55
27	698	((FLIP adj flop with (sleep power adj down active normal standby stand adj by)) same transistor) not (((327/218).CCLS.) ((327/202).CCLS.) ((327/203).CCLS.))	USPAT; US-PGPUB	2004/08/16 14:55
28	1	("6304122").PN.	USPAT; US-PGPUB	2004/08/16 15:27
29	154	latch same first adj supply	USPAT; US-PGPUB	2004/08/16 15:27
30	124	(latch same first adj supply) and transistor	USPAT; US-PGPUB	2004/08/16 15:36

31	1358	data adj latch same (sleep inactive active power adj down standby normal)	USPAT;	2004/08/16 17:18
32	78	31.clm.	US-PGPUB	2004/08/16 15:46
35	6	antennna.clm.	USPAT;	2004/08/16 15:55
36	37096	antenna.clm.	US-PGPUB	2004/08/16 15:56
37	3901	antenna.clm. and processor.clm.	USPAT;	2004/08/16 16:00
38	923	(antenna.clm. and processor.clm.) and wireless.clm.	US-PGPUB	2004/08/16 15:56
39	263	((antenna.clm. and processor.clm.) and wireless.clm.) and interface.clm.	USPAT;	2004/08/16 15:56
40	7	((antenna.clm. and processor.clm.) and wireless.clm.) and interface.clm.) and (latch\$3 flip adj flop\$1).clm.	US-PGPUB	2004/08/16 15:57
41	609	(antenna.clm. and processor.clm.) and interface.clm.	USPAT;	2004/08/16 16:00
42	10	((antenna.clm. and processor.clm.) and interface.clm.) and (latch\$1 flop adj flop\$1).clm.	US-PGPUB	2004/08/16 16:00
43	12	((antenna.clm. and processor.clm.) and interface.clm.) and (latch\$1 flip adj flop\$1).clm.	USPAT;	2004/08/16 16:01
44	7	((antenna.clm. and processor.clm.) and interface.clm.) and (latch\$1 flip adj flop\$1).clm.) not (((antenna.clm. and processor.clm.) and wireless.clm.) and interface.clm.) and (latch\$3 flip adj flop\$1).clm.)	US-PGPUB	2004/08/16 16:02
45	0	wireless.clm. and (latch\$1 flip adj flop\$1).clm.	USPAT;	2004/08/16 16:03
46	276	wireless.clm. and (latch\$1 flip adj flop\$1).clm.	US-PGPUB	2004/08/16 16:03
47	121	(wireless.clm. and (latch\$1 flip adj flop\$1).clm.) and interface	USPAT;	2004/08/16 16:09
48	0	mtcoms	US-PGPUB	2004/08/16 16:09
49	159	mtcmos	USPAT;	2004/08/16 16:48
50	936	latch same save	US-PGPUB	2004/08/16 16:48
51	266	(latch same save) and restor\$3	USPAT;	2004/08/16 17:08
52	1205	flip adj flop same sav\$3	US-PGPUB	2004/08/16 17:08
53	323	(flip adj flop same sav\$3) and restor\$3	USPAT;	2004/08/16 17:08
54	0	((flip adj flop same sav\$3) and restor\$3) and transistpr	US-PGPUB	2004/08/16 17:08
55	189	((flip adj flop same sav\$3) and restor\$3) and transistor	USPAT;	2004/08/16 17:11
56	5	balloon adj latch	US-PGPUB	2004/08/16 17:11
57	64	(latch flip adj flop) and sav\$3 and restor\$3	USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/08/16 17:18